

Electronic Acknowledgement Receipt

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Application Number:	10685762
International Application Number:	
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Title of Invention:	Parallel instances of a plurality of systems on chip in hardware emulator verification
First Named Inventor/Applicant Name:	Shiv Kumar Gupta
Customer Number:	23446
Filer:	Mirut P. Dalal/LaTanya Williamson
Filer Authorized By:	Mirut P. Dalal
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1		15164US01_Response_to_1 _16_07_OA.pdf	137304	yes	5

Multipart Description/PDF files in .zip description			
	Document Description	Start	End
	Amendment - After Non-Final Rejection	1	1
	Claims	2	3
	Applicant Arguments/Remarks Made in an Amendment	4	5

Warnings:

Information:

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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